EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	14	(input adj circuit) (output circuit) (data adj path) (shift adj register adj chain) sequential	US-PGPUB; USPAT; DERWENT	AND	ON	2008/02/05 14:59
L2	4	(input adj circuit) (output adj circuit) (data adj path) (shift adj register adj chain) sequential	US-PGPUB; USPAT; DERWENT	AND	ON	2008/02/05 14:58
L3	0	(input adj circuit) (output adj circuit) (data adj path) (shift adj register adj chain) sequential FSM	US-PGPUB; USPAT; DERWENT	AND	ON	2008/02/05 14:58
L4	1	(input adj circuit) (output adj circuit) (data adj path) (shift adj register adj chain) sequential (finite adj state adj machine)	US-PGPUB; USPAT; DERWENT	AND	ON	2008/02/05 15:00
L5	17	(input) (output) (data adj path) (shift adj register adj chain) sequential (finite adj state adj machine)	US-PGPUB; USPAT; DERWENT	AND	ON	2008/02/05 15:00
L6	15	(input) (output) (data adj path) (shift adj register adj chain) sequential (finite adj state adj machine) multiplexing	US-PGPUB; USPAT; DERWENT	AND	ON	2008/02/05 17:45
L7	1356	(370/535).CCLS.	US-PGPUB; USPAT; DERWENT	OR	OFF	2008/02/05 17:47
L8	1	("0000017").PN.	US-PGPUB; USPAT; DERWENT	OR	OFF	2008/02/05 17:48
L9	1	1 7	US-PGPUB; USPAT; DERWENT	AND	ON	2008/02/05 17:49
L10	1	5 7	US-PGPUB; USPAT; DERWENT	AND	ON	2008/02/05 17:49